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PATENT

I hereby certify that on the date specified below, this correspondence is being sent via facsimile to Examiner B. Peikari at 703-746-7239 with the United States Patent Office.

February 28, 2003 Denise Sheridan
Date Denise Sheridan

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant	: Brent Keeth	Attorney Docket No.:	500514.01 (660073.587)
Serial No.	: 08/798,227	Group Art Unit	: 2186
Filed	: February 11, 1997	Examiner	: Behzad Peikari
Title	: MEMORY SYSTEM WITH DYNAMIC TIMING CORRECTION		

7/30/03

Box Non-Fee Amendment
Commissioner of Patents
Washington, DC 20231

RESPONSE

Sir:

Applicant acknowledges receipt of the Office Action dated December 23, 2002.

REMARKS

Claims 1-24 have been presented for examination and have been rejected under 35 U.S.C. §103(a) as being obvious over U.S. Patent No. 5,673,005 to Pricer, U.S. Patent No. 5,550,783 to Stephens, Jr., *et al.*, U.S. Patent No. 5,229,929 to Shimizu *et al.*, U.S. Patent No. 5,182,524 to Hopkins, U.S. Patent No. 5,130,565 to Girmay, or U.S. Patent No. 4,746,996 to Furuhashi *et al.*

Applicant wishes to thank Examiner Peikari for his time and courtesy in conducting a telephone interview with applicant's attorney on February 26, 2003. During the interview, applicant's attorney explained the manner in which the claims distinguish over the cited references. For example, applicant's attorney pointed out that none of the cited references, taken either alone or in combination, disclose the concept of a feedback control system to control